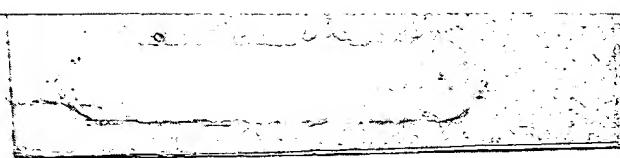


ANALYSIS

08/01/01  
09/920180  
JCG71 u.s. pro

		<b>Subclass</b>
	<b>Class</b>	<b>ISSUE CLASSIFICATION</b>



**PATENT NUMBER**

**U.S. UTILITY Patent Application**

**O.I.P.E.**

PATENT DATE

APPLICATION NO. 09/920180	CONT/PRIOR D	CLASS 713	SUBCLASS 300	ART UNIT 2181	EXAMINER <i>Awe</i>
APPLICANTS		Gary Swoboda 2115 Chandrasekhar			
TITLE Apparatus and method for bus power measurement in a digital signal processor					

## **ISSUING CLASSIFICATION**

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner) _____ (Date)				
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE	
	(Primary Examiner) _____ (Date)			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner) _____ (Date)				

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